



Docket No.: M4065.0900/P900  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Ramin Ghodsi

Application No.: 10/822,785

Confirmation No.: 3196

Filed: April 13, 2004

Art Unit: 2824

For: MULTI-CELL RESISTIVE MEMORY  
ARRAY ARCHITECTURE WITH SELECT  
TRANSISTOR

Examiner: E. J. Wendler

**INFORMATION DISCLOSURE STATEMENT (IDS)**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage of a PCT application, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Office Action or Notice of Allowance (37 CFR 1.97(c)).

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In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

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Dated: April 10, 2006

Respectfully submitted,

By 

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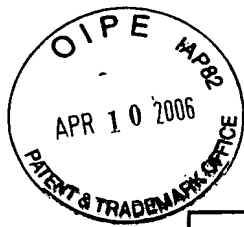
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PTO/SB/08a/b (07-05)

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<b>Substitute for form 1449A/B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	10/822,785-Conf. #3196	
			Filing Date	April 13, 2004	
			First Named Inventor	Ramin Ghodsi	
			Art Unit	2824	
			Examiner Name	E. J. Wendler	
Sheet	1	of	1	Attorney Docket Number	M4065.0900/P900

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	AA	US-2003/0032254 A1	02-13-2003	Gilton	
	AB	US-2003/0045049 A1	03-06-2003	Campbell, et al.	
	AC	US-2003/0001229 A1	01-02-2003	Moore, et al.	
	AD	US-2002/0123248 A1	09-05-2002	Moore, et al.	
	AE	US-2002/0123170 A1	09-05-2002	Moore, et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
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